Parity Effect and Tunnel Magnetoresistance of Ferromagnet / Superconductor / Ferromagnet Single-Electron Tunneling Transistors

Hiroshi Imamura, Yasuhiro Utsumi and Hiromichi Ebisawa Graduate School of Information Sciences, Tohoku University, Sendai 980-8579, Japan

We theoretically study the tunnel magnetoresistance (TMR) of ferromagnet / superconductor / ferromagnet single-electron tunneling transistors with a special attention to the parity effect. It is shown that in the plateau region, there is no spin accumulation in the island even at finite bias voltage. However, the information of the injected spin is carried by the excess electron and thus the TMR exists. The spin relaxation rate of the excess electron can be estimated from the TMR. We also show that the TMR increases with decreasing the size of the superconducting island.

PACS numbers: 72.25.-b,73.23.Hk,74.50.+r,73.23.-b

Single-electron tunneling(SET) transistor is a key device of "single electronics" since the transfer of a single electron can be controlled by gate and bias voltages [1, 2]. The important quantity of the SET transistor is the electrostatic energy of excess electrons in the island called "charging energy", which has a significant effect on charge transport, i. e., the Coulomb blockade(CB) [1]. In the CB region, sequential tunneling, where tunneling events in each junction occur independently, is blocked at T=0 due to the increase of the charging energy. Recently much attention has been devoted to the SET transistor with a superconducting island and normal conducting electrodes(N/S/N) [3, 4, 5, 6, 7, 8]. In a superconducting island, where Cooper pairs form the condensate, the addition of one extra electron costs the superconducting gap energy Δ . This leads to the parity effect: physical properties of the system depend on the parity of the electron number in the island. In SET transistors consisting of normal conducting islands and electrodes (N/N/N) the current depends e periodically on the gate charge. However, the tunneling current of the N/S/N SET transistor is expected to be 2e periodic in the gate charge below the crossover temperature due to the parity effect[3]. The clear signature of 2e periodicity for the N/S/N SET transistor was observed[6, 7].

The research on the N/S/N SET transistor has focused primary on the charge degrees of freedom of electrons, by contrast, its spin degrees of freedom have not yet received much attention. However, an increasing number of researches on spin-electronics show that the spin of electron offers unique possibilities for finding novel mechanisms for future spin-electronic devices [9, 10]. The spinpolarized current injected from a ferromagnetic(F) electrode into the N or S island gives rise to a nonequilibrium spin density in the island. In the F/N/F SET transistor, the tunnel magnetoresistance (TMR) is brought about by spin accumulation in the island [11, 12, 13, 14]. Recently Takahashi et al. have studied the magnetoresistive effects of the F/S/F double tunnel junction caused by competition between superconductivity and spin accumulation [15]. They have shown that the F/S/F double tunnel junction is a magnetoresistive device to control superconductivity by applying the bias voltage or current. The

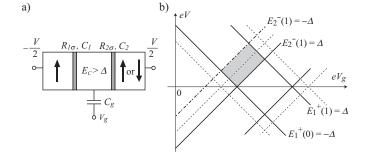


FIG. 1: a)Schematic diagram of a F/S/F SET transistor. The arrows indicate the magnetizations of the left and right electrodes. b) The gate and bias voltage diagram of a symmetric SET transistors. Solid lines indicate the boundaries for the F/S/F SET transistor. Thin dashed lines are the boundaries for the F/N/F SET transistor ($\Delta=0$). The dot-dashed line indicates a boundary of the plateau region.

suppression of the superconductivity by the spin injection in the double tunnel junction was observed by Dong *et al.* [16].

In this article, we theoretically study the spindependent transport of the F/S/F SET transistors at zero temperature with a special attention to the parity effect. We assume that the charging energy E_C is larger than Δ and neglect the two-electron tunneling [17] which becomes important in the opposite situation $(E_C < \Delta)$. In such a system, we have the special region called "plateau region" where the tunneling current is dominated by the transition rate from the odd-state to the even-state [3]. We show that in the plateau region, spin accumulation is forbidden by the superconducting gap even at finite bias voltage. However, the information of the injected spin is carried by the excess electron and the TMR exists. The spin relaxation rate of the excess electron can be estimated from the TMR. We also show that the TMR increases as the size of the island decreases.

We consider the F/S/F SET transistor shown in Fig. 1 (a). For simplicity we assume that the insulating barriers of junctions 1(left) and 2(right) are the same; we subsequently set $C_1 = C_2 \equiv C$. We also assume that the

left and right electrodes are made of the same material with the spin polarization P.

The Hamiltonian of the SET transistor is given by

$$H = H_L + H_I + H_R + H_{ch} + H_{TL} + H_{TR}, (1)$$

where H_L , H_I , and H_R are the Hamiltonians of the left electrode, the central island, and the right electrode, respectively. The Hamiltonian H_{ch} represents the charging energy and the tunneling processes are described by

$$H_{TL(R)} = \sum_{k,q,\sigma} T_{kq}^{L(R)} c_{k\sigma}^{\dagger} c_{q\sigma} + \text{h.c.}, \qquad (2)$$

where the subscript k indicates the wave vector in the left(right) electrode while q represents that in the island and no spin flip is assumed in the tunneling process.

The energy change due to the forward tunneling of an electron with spin σ through the junctions 1 and 2 are respectively given by

$$E_1^+(n) = (1+2n)E_c - \frac{C_g}{C_{\Sigma}}eV_g - \frac{1}{2}eV$$
 (3)

$$E_2^-(n) = (1-2n)E_c + \frac{C_g}{C_{\Sigma}}eV_g - \frac{1}{2}eV,$$
 (4)

where n is the number of excess electrons of the initial state in the island, $C_{\Sigma} \equiv 2C + C_g$, and the superscripts \pm implies that the number of excess electrons in the final state is $n \pm 1$. The energy changes for the backward tunneling are given by $E_1^-(n) = E_2^-(n) + eV$ and $E_2^+(n) = E_1^+(n) + eV$.

In ordinary tunnel junctions with negligible charging effect, individual electrons are unstable for the forward tunneling across the junction, and lower their energy by the bias potential (-eV/2 < 0). In SET transistors, however, there are special regions called CB region in the gate and bias voltage diagram, where the charge state of the island with n excess electrons is stable with respect to tunneling. For the symmetric SET transistor with a normal conducting island, the CB regions are the rhombuses determined by $E_1^+(n), E_1^-(n), E_2^+(n)$, and $E_2^-(n) \geq 0$. The boundaries of CB regions are indicated by thin dashed lines in Fig. 1 (b).

For the superconducting island, however, it is know that these boundaries are modified by superconductivity. In order to obtain the modified CB regions, we evaluate the tunneling rates following Fazio and Schön [3]. Since the island is in the superconducting state, it is convenient to rewrite the electron operators in the island in terms of the quasiparticle operators by using the Bogoliubov transformations. Then the transition rates are determined by using the Golden-rule arguments [3]. Let us first consider an electron tunnels from the left electrode into the island with n=0, thereby changing the electron number from n=0 to n=1 with the rate

$$\Gamma_{1\sigma}^{+}(0) = \frac{1}{e^{2}R_{1\sigma}} \int_{-\infty}^{\infty} dE \int_{-\infty}^{\infty} dE' \mathcal{D}(E') \times f(E) \left[1 - f(E')\right] \delta(E' - E + E_{1}^{+}(0)),$$
(5)

where $\mathcal{D}(E) \equiv |E|/\sqrt{E^2-\Delta^2}$ is the normalized BCS density of states and f(E) is the Fermi distribution function. The tunnel conductance of the junction is defined as $R_{1\sigma}^{-1} \equiv (4\pi e^2/\hbar)N_{\sigma}^I N_{\sigma}^L |T|^2$, where the tunnel matrix elements T_{kq}^L and T_{kq}^R are considered as a constant T and $N_{\sigma}^{I(L)}$ denotes the density of states of the island in the normal conducting state(left electrode). Since we assume the temperature is zero, the Fermi distribution function can be replaced by the step function and we have

$$\Gamma_{1\sigma}^{+}(0) = \begin{cases} 0 & (E_1^{+}(0) \ge -\Delta) \\ \frac{\sqrt{E_1^{+}(0)^2 - \Delta^2}}{e^2 R_{1\sigma}} & (E_1^{+}(0) < -\Delta) \end{cases} . \tag{6}$$

Therefore, the boundary of the CB region for n=0 given by $E_1^+(0)=0$ is lifted to the solid line determined by $E_1^+(0)=-\Delta$ as shown in Fig. 1 (b). The other three boundaries also shift outward and the CB region is determined by $E_1^+(0), E_1^-(0), E_2^+(0)$, and $E_2^-(0) \ge -\Delta$.

Next we consider an electron tunnels from the island with n = 1 to the right electrode, thereby changing the electron number from n = 1 to n = 0 with the rate

$$\Gamma_{2}^{-}(1) = \frac{1}{e^{2}R_{2\sigma}} \int_{-\infty}^{\infty} dE \int_{-\infty}^{\infty} dE' \mathcal{D}(E) \times f(E - \delta\mu) \left[1 - f(E')\right] \delta(E' - E + E_{2}^{-}(1)),$$
(7)

where $R_{2\sigma}^{-1} = (4\pi e^2/\hbar) N_{\sigma}^I N_{\sigma}^R |T|^2$ and $\delta\mu$ is the shift of the chemical potential fixed by the constraint of one excess electron charge in the island [3]. The shift of the chemical potential is $\delta\mu = \Delta$ and we have

$$\Gamma_{2\sigma}^{-}(1) = \begin{cases}
0 & (E_{2}^{-}(1) \ge \Delta) \\
\frac{d}{e^{2}R_{2\sigma}} & (|E_{2}^{-}(1)| < \Delta) \\
\frac{d}{e^{2}R_{2\sigma}} + \frac{\sqrt{E_{2}^{-}(1)^{2} - \Delta^{2}}}{e^{2}R_{2\sigma}} & (E_{2}^{-}(1) \le -\Delta)
\end{cases} , (8)$$

where $d=1/N_{\sigma}^{I}$ is the average level spacing of the island. Therefore, the boundary of the CB region for n=1 given by $E_{2}^{-}(1)=0$ is also moved to the solid line determined by $E_{2}^{-}(1)=\Delta$ as shown in Fig. 1 (b). The other three boundaries also shift inward and the CB region is determined by $E_{1}^{+}(1), E_{1}^{-}(1), E_{2}^{+}(1)$, and $E_{2}^{-}(1) \geq \Delta$. One can easily show that the CB regions for the other even(odd) states are spread(squeezed) like that for n=0(1).

Adjacent to the CB region, we have a so-called "plateau region" where the tunneling current is dominated by the tunneling rate through one junction which behaves like a bottleneck of the tunneling current. In the plateau region the tunneling current is carried by the even and the odd states in the following manner. An electron with spin σ tunnels into the island from the left electrode. While staying in the island, the spin of the electron relaxes due to the spin orbit interaction, surface

scattering, and/or the hyperfine contact interaction [18]. After a certain time period determined by the tunneling rate $\Gamma_{2\sigma}^-(1)$, the electron tunnels out of the island. Therefore, no spin accumulation occurs even at the finite bias voltage. Outside the plateau region, continuous quasiparticle states contribute to the tunneling current and the spin accumulation can exist.

We consider the tunneling current and TMR in the plateau region, for example, indicated by shade in Fig. 1 (b), which is determined by $E_1^+(0) < -\Delta$, $E_1^+(1) > \Delta$, and $|E_2^-(1)| < \Delta$. In this region, the following three states are available: $|0\rangle$ three is no excess electron in the island, $|\uparrow\rangle$ there is one up-spin excess electron in the island, $|\downarrow\rangle$ there is one down-spin excess electron in the island. The transition rate from $|0\rangle$ to $|\sigma\rangle$ is given by

$$\Gamma_{\sigma}^{+} \equiv \Gamma_{1\sigma}^{+}(0) = \frac{1}{e^{2}R_{1\sigma}}\sqrt{E_{1}^{+}(0)^{2} - \Delta^{2}},$$
 (9)

and the transition rate from $|\sigma\rangle$ to $|0\rangle$ is

$$\Gamma_{\sigma}^{-} \equiv \Gamma_{2\sigma}^{-}(1) = \frac{d}{e^2 R_{2\sigma}}.$$
 (10)

We also introduce the spin relaxation rate η which corresponds to the transition rate between $|\uparrow\rangle$ and $|\downarrow\rangle$.

In order to obtain the tunneling current, we construct the master equation for the probabilities of states p_0 , p_{\uparrow} , and p_{\downarrow} , which is given by

$$\dot{p}_0 = \Gamma_{\uparrow}^- p_{\uparrow} + \Gamma_{\downarrow}^- p_{\downarrow} - \left(\Gamma_{\uparrow}^+ + \Gamma_{\downarrow}^+\right) p_0 \tag{11}$$

$$\dot{p}_{\sigma} = \eta \left(p_{\bar{\sigma}} - p_{\sigma} \right) + \Gamma_{\sigma}^{+} p_{0} - \Gamma_{\sigma}^{-} p_{\sigma} \tag{12}$$

with the normalization condition $p_0 + p_{\uparrow} + p_{\downarrow} = 1$. We calculate the stationary probabilities by requiring $\dot{p}_0 = \dot{p}_{\uparrow} = \dot{p}_{\downarrow} = 0$. The solutions are

$$p_0 = \frac{\eta(\Gamma_{\uparrow}^- + \Gamma_{\downarrow}^-) + \Gamma_{\uparrow}^+ \Gamma_{\downarrow}^+}{\gamma}$$
 (13)

$$p_{\sigma} = \frac{\eta \left(\Gamma_{\sigma}^{+} + \Gamma_{\bar{\sigma}}^{+}\right) + \Gamma_{\sigma}^{+} \Gamma_{\bar{\sigma}}^{-}}{\gamma}, \tag{14}$$

where $\gamma = \sum_{\sigma=\uparrow\downarrow} \eta \left(2\Gamma_{\sigma}^{+} + \Gamma_{\sigma}^{-}\right) + \Gamma_{\sigma}^{+}\Gamma_{\bar{\sigma}}^{-} + \frac{1}{2}\Gamma_{\sigma}^{-}\Gamma_{\bar{\sigma}}^{-}$ and the subscript $\bar{\sigma}$ represents the spin direction opposite to σ . The similar result has been obtained for the single discrete level system[19]. Knowing the stationary probabilities we can calculate the tunneling current through the left junction

$$I = -ep_0\left(\Gamma_{\uparrow}^+ + \Gamma_{\downarrow}^+\right). \tag{15}$$

The tunneling current of the F/S/F SET transistor given by Eq. (15) depends strongly on whether the magnetizations in ferromagnetic electrodes are parallel or antiparallel. For the ferromagnetic(F)-alignment, where the magnetizations are parallel, the junction resistances can be expressed as $R_{1\uparrow} = R_{2\uparrow} = R_M$ and $R_{1\downarrow} = R_{2\downarrow} = R_m$. Here R_M is the junction resistance for electrons in

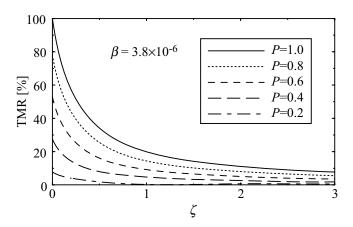


FIG. 2: TMR of the F/S/F SET with $\beta = 3.8 \times 10^{-6}$ is plotted against the normalized spin relaxation rate ζ . From top to bottom: P = 1.0, 0.8, 0.6, 0.4, and 0.2.

the majority spin band and $R_m=(1-P)/(1+P)R_M$ is for those in the minority spin band. We write the transition rates by using the subscripts M and m as $\Gamma_{\uparrow}^{\pm}=\Gamma_{M}^{\pm}$ and $\Gamma_{\downarrow}^{\pm}=\Gamma_{m}^{\pm}$. Introducing $\alpha\equiv R_m/R_M$, $\beta\equiv\Gamma_{M}^{-}/\Gamma_{M}^{+},\zeta\equiv\eta/\Gamma_{M}^{-}$, the probability of the state $|0\rangle$ for the F-alignment is expressed by

$$p_0^F = \frac{\zeta\beta(\alpha+1) + \alpha\beta}{\zeta(\alpha+1)(\beta+2) + \alpha(\beta+2)} = \frac{\beta}{\beta+2}.$$
 (16)

The probability p_0^F is independent of the spin relaxation rate. For the antiferromagnetic(A)-alignment, where the magnetizations are antiparallel, the probability of the even state is

$$p_0^A = \frac{\zeta \beta(\alpha+1) + \alpha \beta}{\zeta(\alpha+1)(\beta+2) + \alpha^2 + \alpha\beta + 1}.$$
 (17)

The TMR is calculated by the formula $TMR = 1 - I_A/I_F$. From Eqs. (15)- (17), we have

$$TMR = \frac{(\alpha - 1)^2}{\zeta(\alpha + 1)(\beta + 2) + \alpha^2 + \alpha\beta + 1}.$$
 (18)

The value β is much smaller than unity in almost all area of the plateau region since $d \ll E_C$. If we assume that $\Delta = 0.18$ meV (Aluminum), $E_C = 8.0$ meV and $d = 3.0 \times 10^{-5}$ meV [4], then we have $\beta = 3.8 \times 10^{-6}$ at the center of the plateau region. In Figure 2, the TMR for $\beta = 3.8 \times 10^{-6}$ is plotted against the normalized spin relaxation rate ζ . The TMR is monotonically decreasing function of ζ and the spin relaxation rate of an excess electron η can be estimated from the TMR by fitting the experimental data. If there is no spin relaxation process in the island, $\zeta = 0$, the TMR is approximately given by $TMR \simeq 2P^2/(1+P^2)$.

The fact that the TMR depends on the spin relaxation rate η via its normalized value ζ means that how much the spin information is transmitted is determined by the

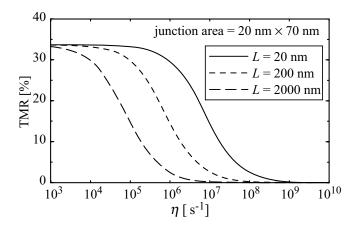


FIG. 3: TMR of the F/S/F SET with P=0.45, which is the typical value for Co [20], is plotted against the spin relaxation rate η . From top to bottom: the length of the island is L=20, 200, and 2000 nm. The area of the junction is fixed at $20\text{nm} \times 70 \text{ nm}[4]$ and the junction resistance $R_M=1 \text{ M}\Omega$ for L=2000 nm. We assume that $\Delta=0.18 \text{ meV}$ (Aluminum), $E_C=8.0\text{meV}$ and $d=3.0\times10^{-5} \text{ meV}$ and the working point is set to the center of the plateau region.

competition between the spin relaxation rate and the tunneling rate through the right junction. In the plateau region the inverse of the tunneling rate $\Gamma^-_{M(m)}$ describes how long the excess electron with majority(minority) spin stays in the island. Therefore, the normalized spin relaxation rate ζ represents the probability that the electron with the majority spin tunnels out of the island holding its spin direction and the TMR is a function of ζ .

In the WKB approximation, the value $|T|^2$ is inversely proportional to the length of the island L [21, 22]. If the junction parameters other than L are kept fixed, the density of states $N_{\sigma}^{I}(=1/d)$ is proportional to L. In this situation, the tunneling rate through the left junction

does not depend on L since the size dependences of $|T|^2$ and N_{σ}^{I} in R_{σ}^{1} cancel out. On the contrary, the tunneling rate through the right junction is inversely proportional to L. The normalized spin relaxation rate ζ decreases and therefore the TMR increases with decreasing L. In Fig. 3 the TMR of the F/S/F SET transistor with P = 0.45, which is the typical value for Co [20], is plotted against the spin relaxation rate η for various values of L. We assume that the area of the junction is fixed at 20 nm \times 70 nm and L=2000 nm, 200, and 20 nm corresponding to the Aluminum island with the average level spacing d = 3.0×10^{-5} , 3.0×10^{-4} , and 3.0×10^{-3} meV, respectively [4]. For the spin relaxation rate $\eta = 10^7 \text{ s}^{-1}$, which is of the same order as that caused by the hyperfine contact interaction [18], the TMR is 0.26, 2.4, and 15 \% for L =2000, 200, and 20 nm, respectively.

Very recently, Chen et al. reported the experimental evidence for suppression of superconductivity by spin imbalance in Co/Al/Co SET transistors[23]. Although they observed the spin imbalance outside the plateau region, their experimental results show that the effect we proposed is relevant to current experiments.

In conclusion, we theoretically study the TMR of F/S/F SET transistors with $E_C > \Delta$. We show that in the plateau region, there is no spin accumulation even at the finite bias voltage. However, the information of the injected spin is carried by the excess electron and the TMR exists. The spin relaxation rate of the excess electron can be estimated from the TMR. We also found that the TMR increases with decreasing the size of the superconducting island.

We would like to thank S. Maekawa and S. Takahashi for valuable discussions. This work was supported by a Grant-in-Aid for Scientific Research (C), No. 14540321 from MEXT. H.I. was supported by MEXT, Grantin-Aid for Encouragement of Young Scientists, No. 13740197.

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